

**bigsim: a parallel simulator for performance prediction of ...** - proceedings of the 18th international parallel and distributed processing symposium (ipdps'04) mpi is a popular way to program parallel machines. however, it remains a question whether it is easy and efficient to program the next generation of supercomputers using it. **2012 ieee 18th international conference on parallel and ...** - data owner files searchable index encrypted files search request cloud server oo data user search resu it **this paper appeared in the proceedings of the 18th ...** - this paper appeared in the proceedings of the 18th international conference on parallel and distributed computing systems, september 12-14, 2005. towards a security service integration framework for distributed real-time systems tao xie xiao qin **in: 18th euromicro international conference on parallel ...** - the parallel parts on simpler cores to achieve higher speedups, ... 18th euromicro international conference on parallel, distributed and network based processing (pdp), pisa, 2010, p. 163-169. ... **peer-to-peer reputations - parallel and distributed ...** - proceedings of the 18th international parallel and distributed processing symposium (ipdps'04) certificates (sdsi certificates [15]) to the peer. using self-certification a peer can generate a large number identities unless a peer is restricted explicitly. identity farms can be- **parallel brutus: the first distributed, fpga accelerated ...** - parallel brutus: the first distributed, fpga accelerated chess program chrilly donninger alex kure ulf lorenz university of paderborn, ... proceedings of the 18th international parallel and distributed processing symposium (ipdps'04) typically, a game playing program consists of three **scalable high-level caching for parallel i/o** - distributed systems use caching to hide the latency and bandwidth of the network. with caching in a distributed environment, there can be multiple cached copies of data, 0-7695-2132-0/04/\$17.00 (c) 2004 ieee proceedings of the 18th international parallel and distributed processing symposium (ipdps'04) **a hierarchical parallel scheme for global parameter ...** - a hierarchical parallel scheme for global parameter estimation in systems biology j. he ... proceedings of the 18th international parallel and distributed processing symposium (ipdps'04) optimization algorithms has become a very active research area. however, the nature of the di- **a scalable architecture for distributed shared memory ...** - a scalable architecture for distributed shared memory multiprocessors using optical interconnects ... distributed-shared memory), we provide high connectivity- ... proceedings of the 18th international parallel and distributed processing symposium (ipdps'04) nels have been implemented[7]. while, in wormhole rout- **papers on parallel and distributed computing** - papers on parallel and distributed computing soo-young lee ... e-beam lithography on a heterogeneous cluster, '04 th 18th international parallel and distributed processing symposium, april 26-30, santa fe, nm, ... ence on parallel and distributed computing systems, pp301-306, september 2002. **processor-embedded distributed mems-based storage systems ...** - processor-embedded distributed mems-based storage systems for high-performance i/o steve c. chiu, wei-keng liao, and alok n. choudhary ... we propose a processor-embedded distributed mems-based storage architecture. ... proceedings of the 18th international parallel and distributed processing symposium (ipdps'04) **... fault tolerant algorithms for orderings and colorings** - fault tolerant algorithms for orderings and colorings ... a goal of a distributed system is that the system should function correctly in spite of intermittent faults. ideally the global state of the system ... proceedings of the 18th international parallel and distributed processing symposium (ipdps'04) **coarse grain reconfigurable architectures - researchgate** - 18th international parallel & distributed processing symposium santa fe, new mexico, usa, april 26 - 30, 2004 reiner hartenstein, university of kaiserslautern, germany **dynamically reconfigurable neuron architecture for the ...** - dynamically reconfigurable neuron architecture for the implementation of self- ... believed to be the result of the parallel and distributed computing performed by approximately 1011 neurons, ... proceedings of the 18th international parallel and distributed processing symposium (ipdps'04)

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